



Stunde	Montag	Dienstag	Mittwoch	Donnerstag	Freitag
<b>1</b>	GE 1 JUCL <b>A0227 + A0228</b> L8 DNBE <b>B0207 + B0208</b> M 2 PNKO <b>B0209 + B0210</b> PL 1 BELA <b>A0203 + A0204</b>	BI 2 DECO <b>Bi209 + Bi207</b> D 2 LKRI <b>B0213 + B0211</b> E 3 SVIC <b>B0209 + B0210</b> IF 3 PNKO <b>EDV121 + EDV123</b> PÄ 1 HÄDO <b>B0201 + B0203</b>	EK 1 WSMA <b>B0202 + B0204</b> VLSI <b>A0201 + A0202</b> PÄ 2 BJAP <b>A0218 + A0220</b> S 1 BZRU <b>A0228 + A0229</b>	GE 1 JUCL <b>A0227 + A0228</b> L8 DNBE <b>B0207 + B0208</b> M 2 PNKO <b>B0209 + B0210</b> PL 1 BELA <b>A0203 + A0204</b>	D LK 2 RNAN <b>B0219 + B0220</b> E LK MDUL <b>A0212 + A0210</b> EK LK PAAL <b>A0218</b> GE LK JUCL <b>B0206</b> PÄ LK HÄDO <b>B0203 + B0201</b> PH LK WINA <b>PH217 + PH219</b> SP LK SDNI <b>B0207 + B0205</b>
<b>2</b>	ER 1 FEAN <b>Bi203 + Bi201</b> KR 1 SPUL <b>B0207 + B0208</b> KR 2 HPDA <b>A0205 + A0208</b> PL 2 FPCA <b>A0201 + A0202</b> PL 3 BELA <b>B0203 + B0205</b>	D 3 RNAN <b>B0219 + B0213</b> EK 2 URMO <b>A0203 + A0204</b> F 1 KSHE <b>B0209 + B0210</b> GE 2 DIHA <b>A0210 + A0212</b> S 2 LACL <b>A0225 + A0224</b>	BI 2 DECO <b>Bi209 + Bi207</b> D 2 LKRI <b>B0213 + B0211</b> E 3 SVIC <b>B0209 + B0210</b> IF 3 PNKO <b>EDV121 + EDV123</b> PÄ 1 HÄDO <b>B0201 + B0203</b>	BI LK HAOL <b>Bi209 + Bi207</b> D LK BSMA <b>A0229 + A0228</b> M LK STJU <b>A0212 + A0210</b> M LK WSMA <b>B0202 + B0204</b> PH LK WFAB <b>PH213 + PH212</b>	D LK 2 RNAN <b>B0213 + B0211</b> E LK MDUL <b>A0212 + A0210</b> EK LK PAAL <b>A0218</b> GE LK JUCL <b>B0206</b> PÄ LK HÄDO <b>B0203 + B0201</b> PH LK WINA <b>PH217 + PH219</b> SP LK SDNI <b>B0207 + B0205</b>
<b>3</b>	BI LK HAOL <b>Bi209 + Bi207</b> D LK BSMA <b>A0229 + A0228</b> E LK STJU <b>A0212 + A0210</b> M LK WSMA <b>B0202 + B0204</b> PH LK WFAB <b>PH213 + PH212</b>	D LK 2 RNAN <b>B0219 + B0220</b> E LK MDUL <b>A0212 + A0210</b> EK LK PAAL <b>A0218</b> GE LK JUCL <b>B0206</b> PÄ LK HÄDO <b>B0203 + B0201</b> PH LK WINA <b>PH217 + PH219</b> SP LK SDNI <b>B0207 + B0205</b>	BI LK HAOL <b>Bi209 + Bi207</b> D LK BSMA <b>A0229 + A0228</b> M LK STJU <b>A0212 + A0210</b> M LK WSMA <b>B0202 + B0204</b> PH LK WFAB <b>PH213 + PH212</b>	D LK 2 RNAN <b>B0219 + B0220</b> E LK MDUL <b>A0212 + A0210</b> EK LK PAAL <b>A0218</b> GE LK JUCL <b>B0206</b> PÄ LK HÄDO <b>B0203 + B0201</b> PH LK WINA <b>PH217 + PH219</b> SP LK SDNI <b>B0207 + B0205</b>	BI 1 BÜAL <b>Bi201 + Bi203</b> E 2 SNBE <b>B0207 + B0208</b> M 1 WOBE <b>PH213 + PH211</b> PH 1 JÜUL <b>PH219 + PH217</b>
<b>4</b>	BI 1 BÜAL <b>Bi201 + Bi203</b> E 2 SNBE <b>B0207 + B0208</b> M 1 WOBE <b>PH213 + PH211</b> PH 1 JÜUL <b>PH219 + PH217</b>	SP 1 AGMA <b>A0218 + A0220</b> SP 2 LACL <b>A0227 + A0226</b> SP 3 VLSI <b>A0201 + A0202</b> SP 4 DJUL <b>B0208 + B0207</b> SP 5 KXLA <b>B0209 + B0210</b>	BI LK HAOL <b>Bi209 + Bi207</b> D LK BSMA <b>A0229 + A0228</b> M LK STJU <b>A0212 + A0210</b> M LK WSMA <b>B0202 + B0204</b> PH LK WFAB <b>PH213 + PH212</b>	D 3 RNAN <b>B0219 + B0213</b> EK 2 URMO <b>A0203 + A0204</b> F 1 KSHE <b>B0209 + B0210</b> GE 2 DIHA <b>A0210 + A0212</b> S 2 LACL <b>A0225 + A0224</b>	KU 1 KNCH <b>KU119 + KU116</b> KU 2 MZDA <i>KU117 + KU113</i> LIT BSMA <b>A0228 + A0229</b> MU 1 BTHE <b>MU120 + MU122</b>

5/6		BI 3 DECO <b>Bi207 + Bi209</b> CH 1 REGE CH 1 KRVE <b>CH214 + CH216</b> D 1 STJU <b>A0212 + A0210</b> M 4 WSMA <b>B0202 + B0204</b> S 3 LACL <b>A0225 + A0224</b>			E 1 SVIC <b>B0209 + B0210</b> KU 3 HOHA <b>KU117 + KU119</b> S 2 LACL <b>A0225 + A0224</b>
7	BI 3 DECO <b>Bi207 + Bi209</b> CH 1 REGE CH 1 KRVE <b>CH214 + CH216</b> D 1 STJU <b>A0212 + A0210</b> M 4 WSMA <b>B0202 + B0204</b> S 3 LACL <b>A0225 + A0224</b>		SP 1 AGMA <b>A0218 + A0220</b> SP 2 LACL <b>A0227 + A0226</b> SP 3 VLSI <b>A0201 + A0202</b> SP 4 DJUL <b>B0208 + B0207</b> SP 5 KXLA <b>B0209 + B0210</b>	ER 1 FEAN <b>Bi203 + Bi201</b> KR 1 SPUL <b>B0207 + B0208</b> KR 2 HPDA <b>A0205 + A0208</b> PL 2 FPCA <b>A0201 + A0202</b> PL 3 BELA <b>B0203 + B0205</b>	
8	E 1 SVIC <b>B0209 + B0210</b> KU 3 HOHA <b>KU117 + KU119</b> S 2 LACL <b>A0225 + A0224</b> S 1 BZRU <b>A0228 + A0229</b>		KU 1 KNCH <b>KU119 + KU116</b> KU 2 MZDA <i>KU117 + KU113</i> LIT BSMA <b>A0228 + A0229</b> MU 1 BTHE <b>MU120 + MU122</b>	EK 1 WSMA <b>B0202 + B0204</b> VLSI <b>A0201 + A0202</b> PÄ 2 BJAP <b>A0218 + A0220</b> S 1 BZRU <b>A0228 + A0229</b>	